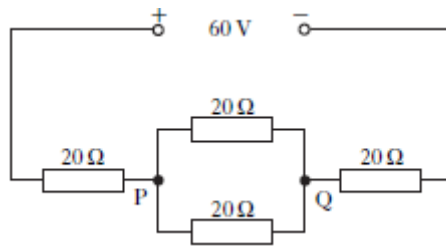


**Electricity Homework One – Part A** (Target mark 10 out of 12)

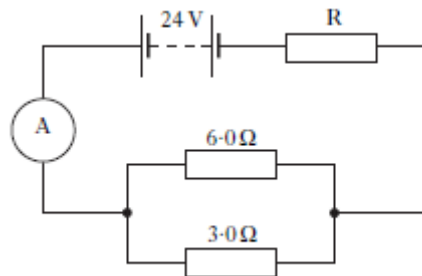
1. Four resistors, each of resistance  $20\Omega$ , are connected to a  $60\text{V}$  supply as shown.



- Calculate the total resistance of the circuit. (6)
- Calculate the current drawn from the supply. (3)
- Calculate the p.d. between P and Q. (3)

**Electricity Homework One - Part B** (Target mark 9 out of 10)

1. The following circuit is set up.



The reading on the ammeter is  $2.0\text{A}$

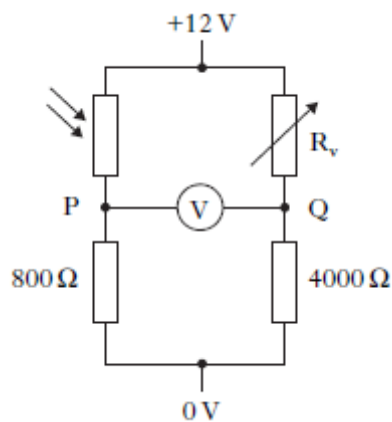
- Calculate the total resistance of the parallel section of the circuit. (3)
- Calculate the p.d. across the parallel section of the circuit. (3)
- State the p.d. across the resistor R. (1)
- Calculate the resistance of R. (3)

**Electricity Homework Two** (Target mark 13/15)

1. If an electric fire uses 0.6MJ of energy in a time of 5minutes, calculate the power output of the fire. (3)
2. If a filament lamp of power rating 60W is used for 2hrs30minutes, calculate how much electrical energy will have been supplied. (3)
3. A toaster connected to the mains supply draws a current of 5A. Calculate the power of the toaster. (3)
4. A  $240\Omega$  resistor is connected to a power output of 0.6W. Calculate how much current will flow through the resistor. (3)
5. A  $4k\Omega$  resistor is connected to a power output of 40W. Calculate the potential difference across the resistor. (3)

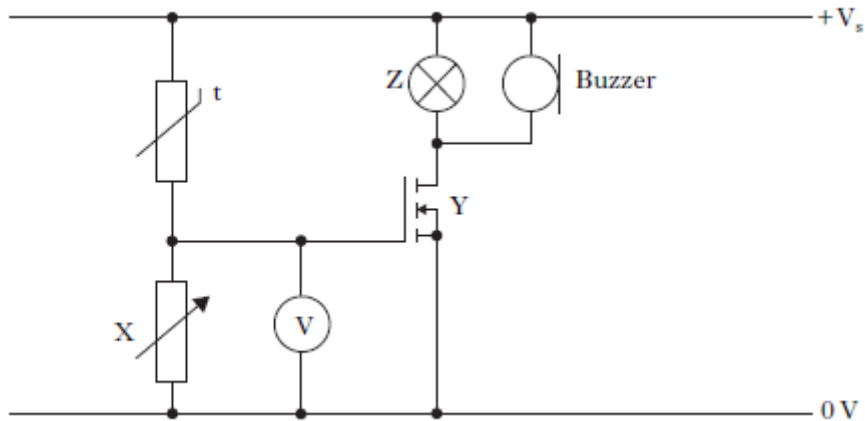
**Electricity Homework Three** (Target mark 9/10)

1. A light sensor consists of an LDR connected in a Wheatstone bridge as shown.



- If the variable resistor,  $R_v$ , is set to  $6000\Omega$  and the LDR has a resistance of  $1600\Omega$ , calculate the p.d. across the  $800\Omega$  resistor. (3)

2. Part of an electronic circuit that controls temperature is shown below.

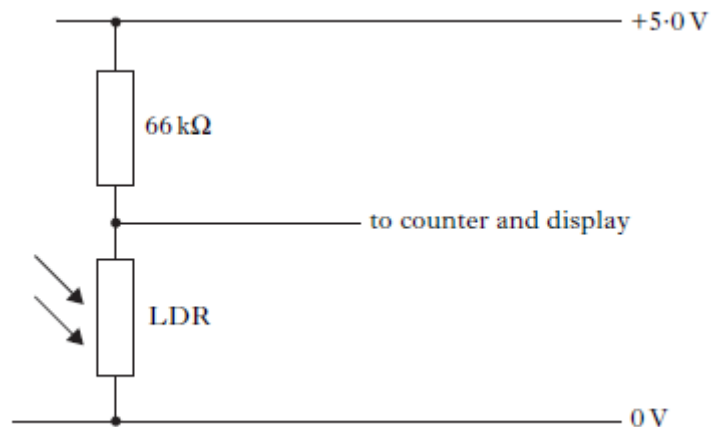


The supply voltage,  $V_s$ , to the circuit is 12V.

When the thermistor has a resistance of  $3\text{k}\Omega$  and the variable resistor is set to  $15\text{k}\Omega$ , calculate the reading on the voltmeter.

(3)

3. Part of an electronic circuit is shown below.



The resistance of the LDR for different conditions is shown in the table.

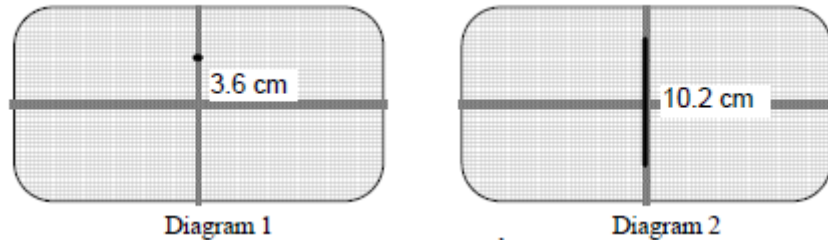
Condition	Resistance of LDR ( $\text{k}\Omega$ )
covered	22
uncovered	2

Calculate the p.d. across the LDR when it is covered.

(4)

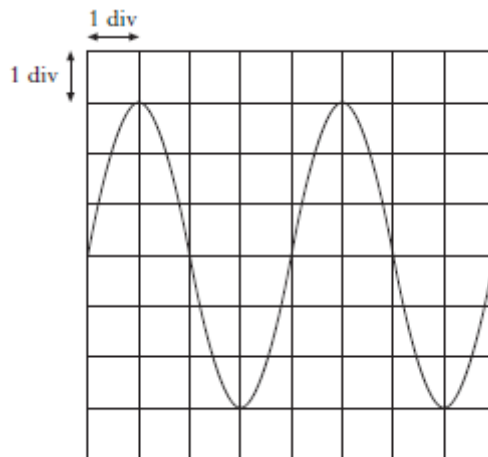
**Electricity Homework Four** (Target mark 9/10)

1. A CRO has its time-base control turned off and is connected to two different power supplies. The traces obtained are shown below.



Explain which diagram - 1 or 2 – was obtained from an a.c. power supply? (2)

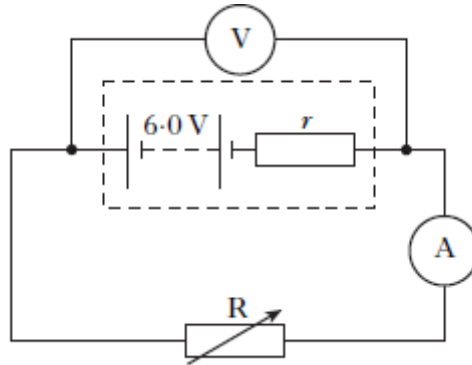
2. A signal from a power supply is to be displayed on an oscilloscope. With the time-base control set to 0.01s/div and the Y-gain set to 4.0V/div the following trace is obtained.



- a) What is the peak voltage for the signal? (Show your working) (2)
- b) Calculate the  $V_{\text{rms}}$  for the power supply. (Show answer to one decimal place) (3)
- c) Calculate the frequency of the signal from the power supply. (3)

**Electricity Homework Five** (Target mark 8/10)

A battery of e.m.f. 6.0V and internal resistance,  $r$ , is connected to a variable resistor,  $R$ , as shown below.



- a) What is meant by an e.m.f. of 6.0V? (1)
- b) When the variable resistor is set to  $1.5\Omega$ , the reading on the ammeter is 3.0A. Under these conditions:
- i) Calculate the terminal potential difference of the circuit. (3)
- ii) State the value of the lost volts in the circuit. (1)
- c) Calculate the internal resistance of the battery. (3)
- d) Would the value of the variable resistor have to increase or decrease to allow maximum power transfer in this circuit? You must justify your answer. (2)

**Electricity Homework Six** (Target mark 17/20)

1. What is meant by the expression “a capacitance of  $5\mu\text{F}$ ”? (1)
2. A 230V dc supply, a  $47\mu\text{F}$  capacitor, a switch and a resistor are connected in a series circuit.
- a) Draw this circuit – assume the switch is open and the capacitor is uncharged. You must use a ruler. (2)
- b) Sketch a graph of charge vs. potential difference for the circuit from when the switch is closed until the capacitor is fully charged. (No numerical values needed but you must use a ruler. Include origin) (1)
- c) State the potential difference across the capacitor when it is fully charged. (1)
- d) Calculate the energy stored in the capacitor when it is fully charged. (3)

### Electricity Homework Six(continued)

3. Draw and clearly label neat graphs, including origins, which illustrate the following:

- a) Current vs time during the charging of a capacitor.
- b) Current vs time during the discharging of a capacitor.
- c) Current vs frequency in a capacitive circuit.
- d) Current vs frequency in a resistive circuit.
- e) Voltage vs time during the charging of a capacitor.
- f) Voltage vs time during the discharging of a capacitor.

(6)

4. A  $200\mu\text{F}$  capacitor is charged until the voltage across its plates is 50V.

- a) Calculate how much charge has been stored during the charging process?

(3)

- b) The capacitor can be discharged in 2.5milliseconds. Calculate the average current during discharge.

(3)

### Electricity Homework Seven (Target mark 7/10)

1. Draw an energy band diagram for each of the following, clearly labelling the conduction band and the valence band in each diagram.

- a) An insulator
- b) A semiconductor
- c) A conductor

(3)

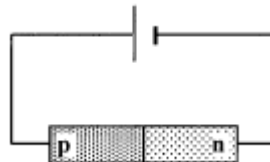
2. What is doping? Your answer may use diagrams and should include the following – impurity, n-type and p-type.

(3)

3.

- a) Is the diode in the following circuit forward biased or reverse biased?

(1)



- b) Explain what happens to the electrons, holes and the depletion layer in the above diode. Your answer must include the terms conduction band and valence band.

(3)